

Serial No.: 10/656,383



Replacement Sheet

ABSTRACT OF THE DISCLOSURE

A circuit arrangement for compensating for disturbances in a signal generated by means of discrete multitone modulation includes a multiplicity of adder circuits that are supplied with an error signal vector that is not used for transmitting data via the transmission channel. The multiplicity of adder circuits adds the error signal vector to at least one signal vector to generate an error-corrected signal vector. The circuit arrangement can also include at least one further multiplicity of adder circuits that are supplied with a further error signal vector, the at least one further multiplicity of adder circuits adding the respective further error signal vector to the at least one signal vector to generate a progressively error-corrected signal vector. The circuit arrangement further includes at least one multiplicity of multiplier circuits that precede the at least one multiplicity of adder circuits and multiply the respective error signal vector by adjustable coefficients.